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Based on		Prepared 2010-05-12	Agneta Rydh	Title Connection Diagram		Doc.des.	Ref.des.	
		Approved 2011-12-08	Patrik Nyback	RET650 (20LTControl) A07		Doc.kind		
		Project		IEC symbols		Resp.dept.	PSNX/TPLA	Rev.ind. B
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Rev	Rev. Note	Date	Name			Sheet	1	
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1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

B

C

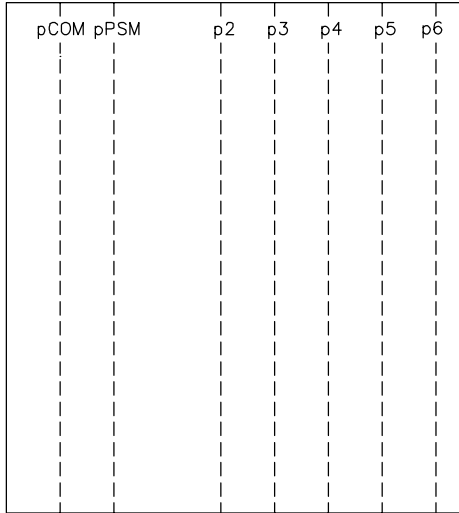
D

E

F

1/2x19"

Rear view slot



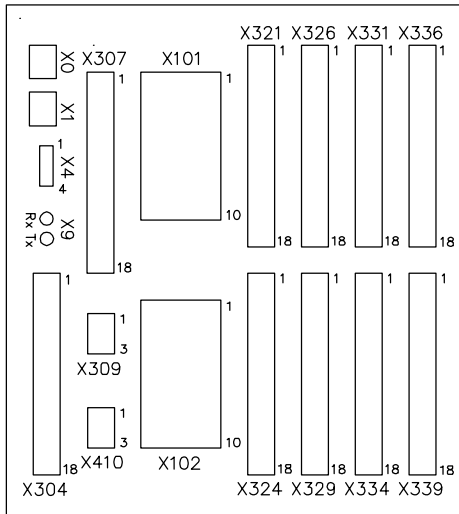
6U

Designation for 6U, 1/2x19" casing with 1 TRM

Module	Slot	Terminal
COM	pCOM	X0, X1, X4, X9, X304
PSM	pPSM	X307, X309, X410
TRM	p2	X101, X102
BIO	p3	X321, X324
BIO	p4	X326, X329
BIO	p5	X331, X334
BIO	p6	X336, X339

p= Position

Rear view terminal

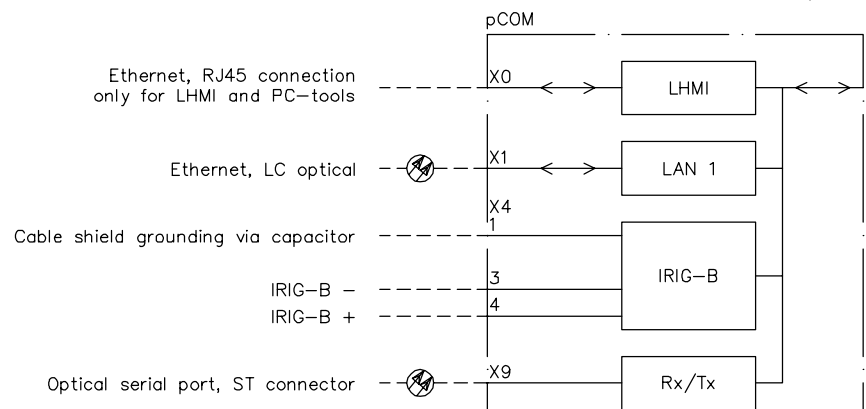


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1 2 3 4 5 6 7 8

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### Communication module (COM)



X304		CONFIGURATION
1	C1-4	
+ 2	BI01	// T1_MANCTRL
+ 3	BI02	// T1_RAISEV
+ 4	BI03	// T1_LOWERV
+ 5	BI04	// T1_AUTOCTRL
+ 6	C5-8	
+ 7	BI05	// T2_MANCTRL
+ 8	BI06	// T2_RAISEV
+ 9	BI07	// T2_LOWERV
+ 10	BI08	// T2_AUTOCTRL
+ 11	C9-11	
+ 12	BI09	// T1_MASTER
+ 13	BI10	// T1_FOLLOWER
+ 14	BI11	// T1_INDEPENDEN
+ 15	C12-14	
+ 16	BI12	// T2_MASTER
+ 17	BI13	// T2_FOLLOWER
+ 18	BI14	// T2_INDEPENDEN



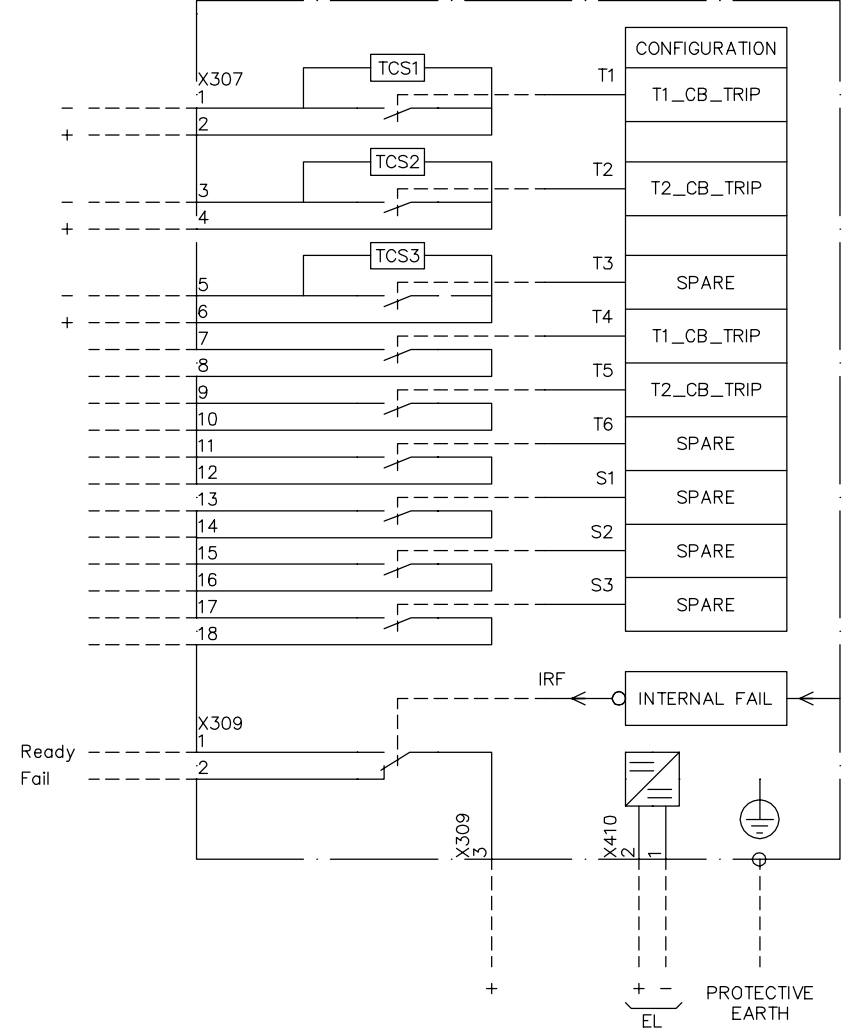
Observe polarity sequence of RL connectors

Based on		Prepared 2010-05-12	Agneta Rydh	Title Connection Diagram		Doc.des.	Ref.des.	
		Approved 2011-12-08	Patrik Nyback	RET650 (20LTControl) A07		Doc.kind		
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1		2						Cont. 4

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### Power supply module 48–125 VDC (PSM)

pPSM



Observe polarity sequence

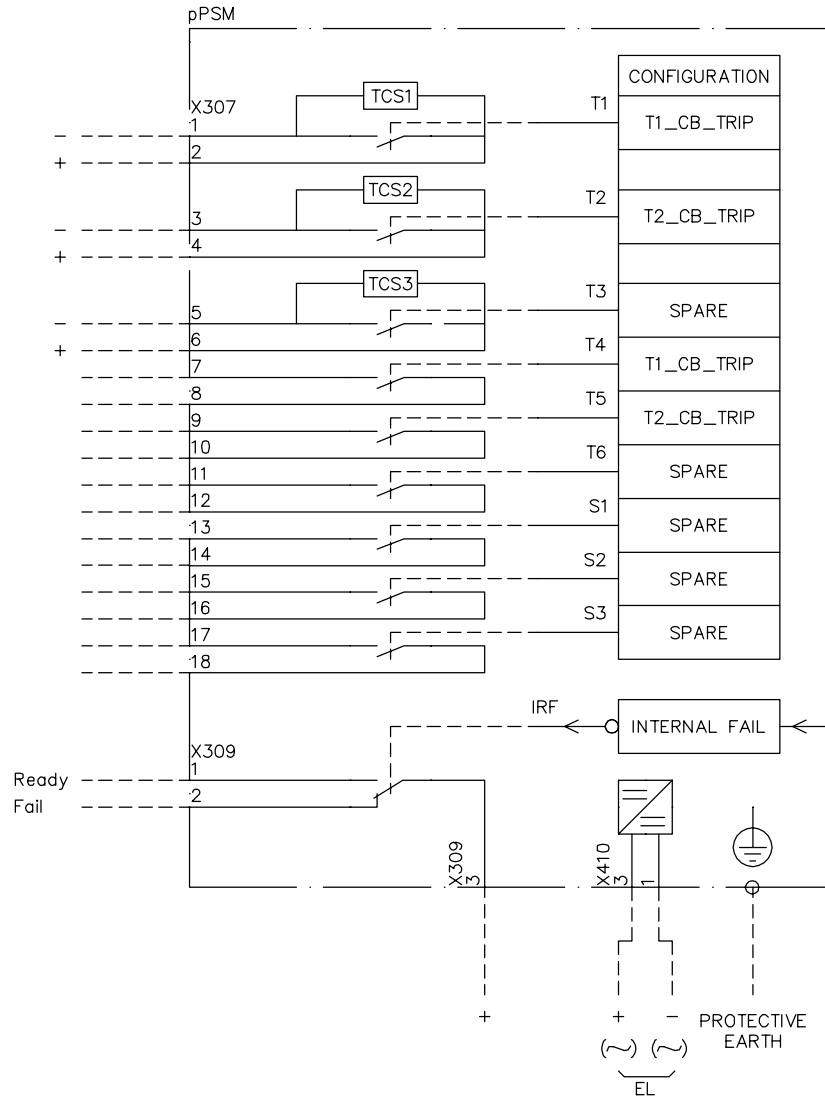
Based on		Prepared 2010-05-12	Agneta Rydh	Title Connection Diagram		Doc.des.	Ref.des.	
		Approved 2011-12-08	Patrik Nyback	RET650 (20LTControl) A07		Doc.kind		
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								Cont. 5

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Power supply module 110–250 VDC, 100–240 VAC (PSM)



Observe polarity sequence

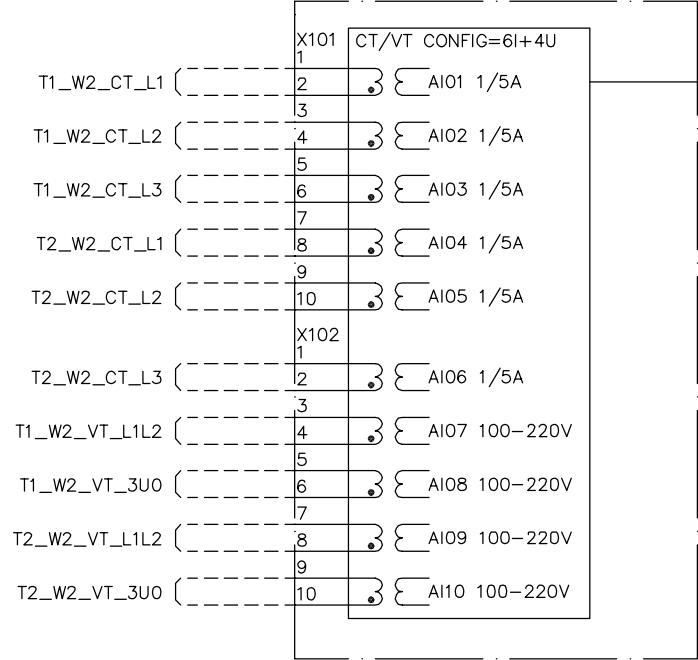


Based on		Prepared 2010-05-12	Agneta Rydh	Title Connection Diagram		Doc.des.	Ref.des.	
		Approved 2011-12-08	Patrik Nyback	RET650 (20LTCCControl) A07		Doc.kind		
		Project		IEC symbols		Resp.dept.	PSNX/TPLA	Rev.ind. B
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						Cont. 6		

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### Transformer module (TRM)

p2



Compression or ringlug terminals

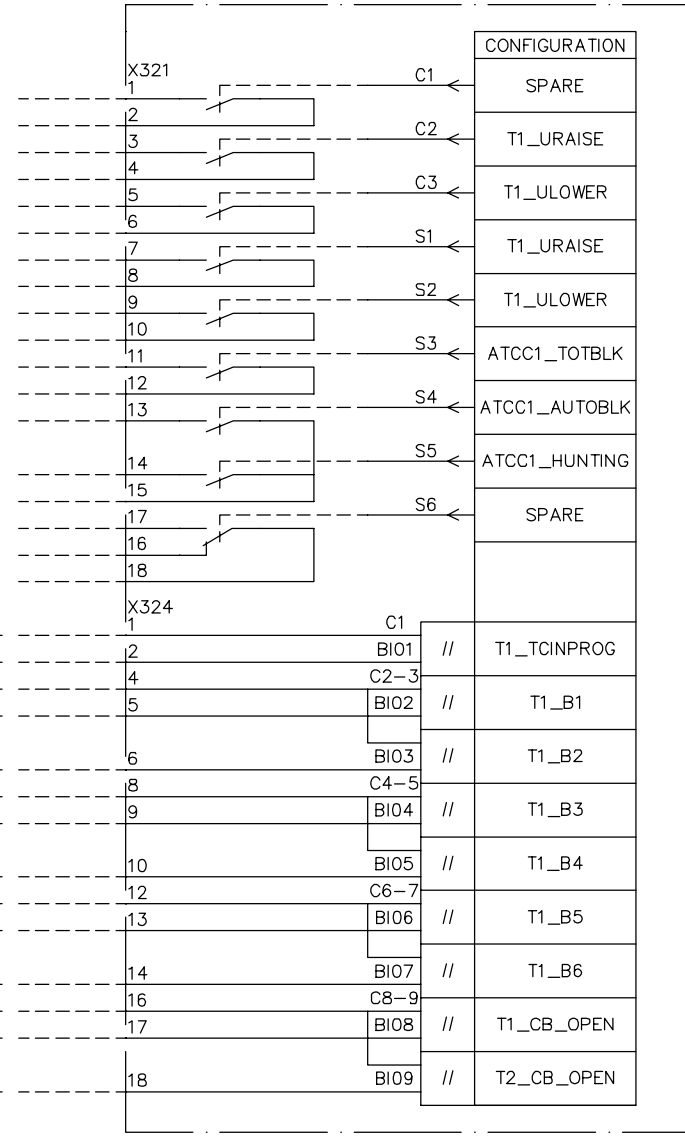
- Indicates high polarity. Note that internal polarity can be adjusted by setting of analog input CT neutral direction and or on SMAI pre-processing function blocks.

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1		2						Cont. 7

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### Binary input/output module (BIO)

p3



Observe polarity sequence of RL connectors

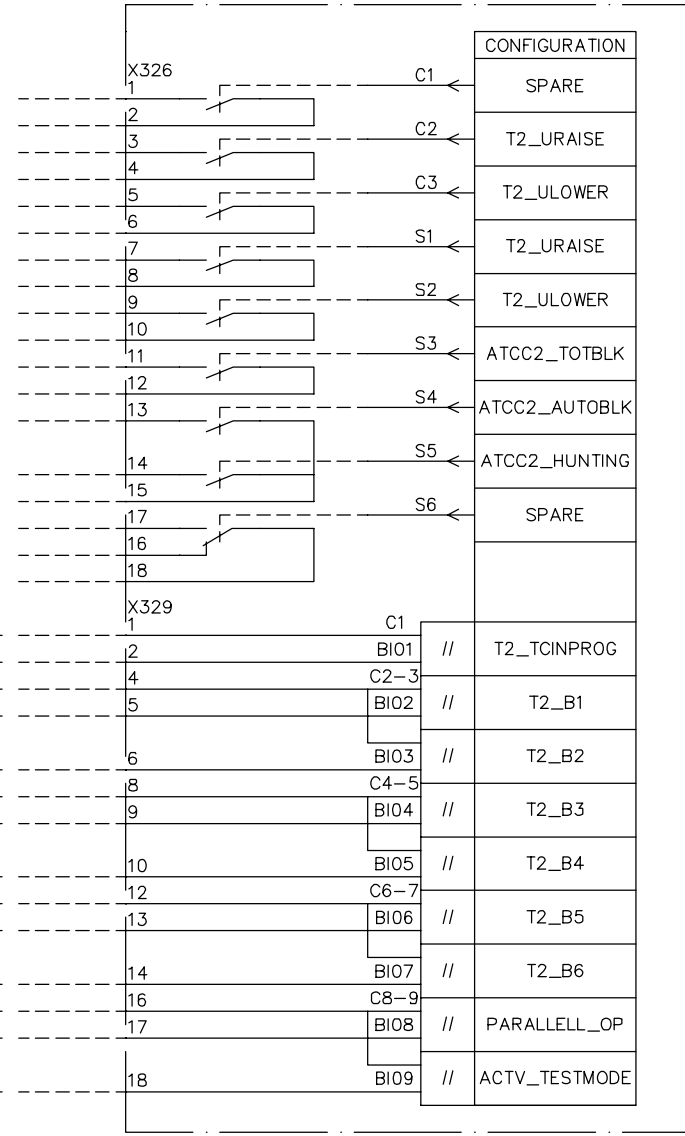
RL

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1		2						Cont. 8

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### Binary input/output module (BIO)

p4



Observe polarity sequence of RL connectors

RL

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		Project		IEC symbols		Resp.dept. PSNX/TPLA		Rev.ind. B	
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