

B515 Taylor Control Language Using Advant OCS

Course Description



Course Duration

The duration is 10 days.

Course Goal

The goal of this course is to teach students the skills required to write Taylor Control Language (TCL) sequences using fundamental and advanced language features. Although this course is written for users of Advant OCS with MOD 300 software, it also applies to the traditional MOD 300 System.

Student Profile

This training is targeted to system/process engineer or system programmer.

Prerequisites and Recommendations

Successful completion of one of the following system engineering courses is required: B115, B400, B405, or B425. Students should also have knowledge of basic process operations and control.

Description

In this course, students will learn about the fundamentals of TCL programming and design. Topics include the development of calculation algorithms, database accessing techniques, unit relative and unit symmetrical sequences, inter-program and intra-program control and communications, mailbox facilities, and sequence control statements for concurrent and independent sequence actions.

Course Objectives

Upon completion of this course, students will be able to:

- Identify database and environment modifications to use TCL.
- Using the MOD 300 System displays, locate the functional level of sequences.
- Develop, debug, test, and execute sequences using the Editor and Runtime Console Support.
- Develop TCL sequences that:
 - Perform process control calculations, manipulate arrays, and access recipes.
 - Perform start-up, shutdown, and emergency actions.
 - Control system sequences and perform sequence activation.
 - Control batch processes.
 - Monitor and access functional elements.
 - Access/modify sequence parameters.
 - Perform batch process data collection and generate batch reports.
 - Access data and string FCM's.



Course Calendar - B515 Taylor Control Language Using Advant OCS

Day 1	Day 2	Day 3	Day 4	Day 5
<ul style="list-style-type: none"> • General Information • MOD 300 Unit Concept • MOD 300 Database • TCL Structure Basics • Editing/Compiling/Linking Lab: <ul style="list-style-type: none"> • Reactor Control • Edit/Compile/Load • Sequence Debugging 	<ul style="list-style-type: none"> • Language Basics <ul style="list-style-type: none"> • Sequence Variables • Unit Message Interface • Sequence Constants • Selection Constructs • Iteration Constructs Lab: <ul style="list-style-type: none"> • Minimal Sequence • Sequence Variables and Constants 	<ul style="list-style-type: none"> • Language Basics (cont.) <ul style="list-style-type: none"> • String Handling • Subroutines • Database Access <ul style="list-style-type: none"> • Database Variables • CCF Loop Access Lab: <ul style="list-style-type: none"> • Operator Input • Selection Constructs • Database Access 	<ul style="list-style-type: none"> • Database Access (cont.) <ul style="list-style-type: none"> • TCL Recipe Access • Taylor Ladder Logic (TLL) Access • Program Control Block Lab: <ul style="list-style-type: none"> • Iteration Constructs • Local Array Variables • Internal Subroutine 	<ul style="list-style-type: none"> • TLL/TCL Interlock applications • Summary and Wrap-up Lab: <ul style="list-style-type: none"> • Reactor Project <ul style="list-style-type: none"> • General Information • Structured Design
Day 6	Day 7	Day 8	Day 9	Day 10
<ul style="list-style-type: none"> • Special Topics <ul style="list-style-type: none"> • TCL Mailbox • Abnormal Processing • Event Processing Lab: <ul style="list-style-type: none"> • Reactor Project (cont.) <ul style="list-style-type: none"> • Operator Interface with Subroutine 	<ul style="list-style-type: none"> • Report Services Interface • History Services Interface Lab: <ul style="list-style-type: none"> • Reactor Project (cont.) <ul style="list-style-type: none"> • Basic Reactor Functionality • Recipe Control 	<ul style="list-style-type: none"> • Advanced Topics <ul style="list-style-type: none"> • External (compiled) Subroutines • User Calculation, Synchronous and Asynchronous Lab: <ul style="list-style-type: none"> • Project Enhancements <ul style="list-style-type: none"> • Sampling Sequence 	<ul style="list-style-type: none"> • Unit Arrays • Peripheral I/O Statements • Sequential Function Chart Statements • Batch 300 Statements Lab: <ul style="list-style-type: none"> • Project Enhancements <ul style="list-style-type: none"> • Shared Sampler • Batch Filr/Report 	<ul style="list-style-type: none"> • TLL/TCL Interlock Applications Lab: <ul style="list-style-type: none"> •